

Marvell 美满电子 2021 校园招聘

Essential technology, done right

招聘职位：模拟集成电路设计

工作地点：上海

About Marvell

At Marvell, we believe that infrastructure powers progress. That execution is as essential as innovation. That better collaboration builds better technology. Trusted by the world's leading technology companies for 25 years, we move, store, process and secure the world's data with semiconductor solutions designed for our customers' current needs and future ambitions. Through a process of deep collaboration and transparency, we're ultimately changing the way tomorrow's enterprise, cloud, automotive, and carrier architectures transform—for the better.

The data infrastructure that our customers build has never been more critical to our global economy. It's what's keeping the world connected, businesses running, and information flowing. If you're ready to excel, innovate, and truly enjoy your work, apply now for the position detailed below.

The Opportunity

Become a member of a world-class analog design team in providing high performance analog and mixed mode circuits and IPs for leading data communications and networking products. Designers have opportunities to design high performance Serdes, analog-to-digital converters (ADC), digital-to-analog converters (DAC), filter, adaptive equalizers, clock and data recovery (CDR) circuits, and phase-locked loop (PLL) or other timing circuits. Team members participate in circuit architecture, circuit implementation, design review, layout, and silicon validation.

Responsibilities:

1. Responsible for Key IP development, including design, guiding layout, debug.
2. Responsible for working closely with marketing, application, product and test engineers
3. Responsible for bench evaluation in Lab for the designed silicon
4. Responsible for coaching the layout engineer
5. Responsible for mix-signal processing products design
6. Other analog design experiences in one or more of the following fields are highly desired:
 - ◇ precision band gap, low noise high precision opamp, precision comparator, switch-capacitor circuit;
 - ◇ Serdes, PLLs, oscillators.
 - ◇ delta-sigma ADC, SAR ADC or other high speed ADC

Qualifications:

1. Solid understanding on circuit analysis, verification and IC design technology
2. Experience using analog simulation tools .
3. Good silicon debug capability .
4. Excellent verbal and written communication skills

5. Ability to work effectively within a team environment

The Perks

With competitive compensation and great benefits, you will enjoy our workstyle within an incredible culture. We'll give you all the tools you need to succeed so you can grow and develop with us. For additional information on what it's like to work at Marvell, visit our [Careers](#) page.

Your Future

Marvell provides a work environment that promotes employee growth and development. We are searching for an individual who wants to grow with the company and will strive to improve performance. If you are driven, personable, and energetic, there will be additional opportunities for you here at Marvell.

All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, national origin, sexual orientation, gender identity, disability or protected veteran status.

请将简历命名为“姓名+学校+岗位”投递至邮箱：448849286@qq.com

或点击链接，投递简历：<https://jinshuju.net/f/kl4UVf>